

Board Connector Pin Map

CN1							
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능
POWER	MAIN_BATT	-	2	1	-	MAIN_BATT	POWER
GND	GND	-	4	3	-	GND	GND
BOOT Mode	XOM2	-	6	5	-	XdacOUT_0	DAC
	XBO0	-	8	7	-	XdacOUT_1	
	XBO5	-	10	9	-	XdacOUT_2	
ADC	XadcAIN7/YP	-	12	11	-	XadcAIN6/YM	ADC
	XadcAIN9/XP	-	14	13	-	XadcAIN8/XM	
HDMI	XhdmITXCP	-	16	15	-	XhdmITXP0	HDMI
	XhdmITXCN	-	18	17	-	XhdmITXN0	
	XhdmITXP1	-	20	19	-	XhdmITXP2	
	XhdmITXN1	-	22	21	-	XhdmITXN2	
LCD	XvHSYNC	GPF0[0]	24	23	GPF0[1]	XvVSYNC	LCD
	XvVCLK	GPF0[3]	26	25	GPF0[2]	XvVDEN	
	XvVD1	GPF0[5]	28	27	GPF0[4]	XvVD0	
	XvVD3	GPF0[7]	30	29	GPF0[6]	XvVD2	
	XvVD5	GPF1[1]	32	31	GPF1[0]	XvVD4	
	XvVD7	GPF1[3]	34	33	GPF1[2]	XvVD6	
	XvVD9	GPF1[5]	36	35	GPF1[4]	XvVD8	
	XvVD11	GPF1[7]	38	37	GPF1[6]	XvVD10	
	XvVD13	GPF2[1]	40	39	GPF2[0]	XvVD12	
	XvVD15	GPF2[3]	42	41	GPF2[2]	XvVD14	
GND	GND	-	44	43	-	GND	GND
LCD	XvVD17	GPF2[5]	46	45	GPF2[4]	XvVD16	LCD
	XvVD19	GPF2[7]	48	47	GPF2[6]	XvVD18	
	XvVD21	GPF3[1]	50	49	GPF3[0]	XvVD20	
	XvVD23	GPF3[3]	52	51	GPF3[2]	XvVD22	
IEM	XiemSCLK	GPI[0]	54	53	GPI[1]	XiemSPWI	IEM
CAMERA	XciRESET	GPE1[4]	56	55	GPE1[5]	XciFIELD	CAMERA
	XciPCLK/MMC1CLK	GPE0[0]	58	57	GPE1[3]	XciMCLK	
	XciHREF/MMC1CMD	GPE0[2]	60	59	GPE0[1]	XciVSYNC/MMC1CDn	

CN2							
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능
POWER	VDD_3.3V	-	2	1	-	VDD_3.3V	POWER
GND	GND	-	4	3	-	GND	GND
UART	XuRTSn1	GPA0[7]	6	5	-	VDD_RTC_AP	POWER
PWM	XpwmTOUT0/TCLK	GPD[0]	8	7	GPA0[3]	XuRTSn0	
UART	XuCTSn0	GPA0[2]	10	9	-	XjTMS	JTAG
JTAG	XjTRSTn	-	12	11	-	XjTCK	
UART	XuCTSn1	GPA0[6]	14	13	-	XjTDI	
SPI	XspiMOSI1	GPB[6]	16	15	-	XjTDO	
	XspiCS1	GPB[7]	18	17	-	XnRESET	
	XspiCS0	GPB[3]	20	19	GPD[4]	Xi2cSCL0	I2C
I2C	Xi2cSDA0	GPD[5]	22	21	GPB[0]	XspiMISO0	SPI
UART	XuRXD3/CTSn2/IRDA_RXD	GPA1[2]	24	23	GPB[1]	XspiCLK0	
	XuTXD3/RTSn2/IRDA_TXD	GPA1[3]	26	25	GPB[2]	XspiMOSI0	
	XuCLK/IRDA_SDBW	GPA1[4]	28	27	GPD[1]	XpwmTOUT1	PWM
I2C	Xi2cSDA1/SPDIF_OUT	GPD[5]	30	29	GPD[2]	XpwmTOUT2	
	Xi2cSCL1/SPDIF_CLK	GPD[6]	32	31	GPA0[0]	XuRXD0	UART
SPI	XspiMISO1	GPB[4]	34	33	GPA0[1]	XuTXD0	
	XspiCLK1	GPB[5]	36	35	GPA0[4]	XuRXD1	
	XmmcCLK0	GPG0[0]	38	37	GPA0[5]	XuTXD1	
	XmmcCMD0	GPG0[1]	40	39	GPA1[0]	XuRXD2	
	XmmcDATA0_0	GPG0[2]	42	41	GPA1[1]	XuTXD2	
	XmmcDATA0_1	GPG0[3]	44	43	-	XnRSTOUT	SYS Fn
GND	GND	-	46	45	-	GND	GND
MMC	XmmcDATA0_2	GPG0[4]	48	47	GPG2[0]	XmmcCLK1	MMC
	XmmcDATA0_3	GPG0[5]	50	49	GPG2[1]	XmmcCMD1	
	XmmcDATA0_4	GPG0[6]	52	51	GPG2[2]	XmmcDATA1_0	
	XmmcDATA0_5	GPG0[7]	54	53	GPG2[3]	XmmcDATA1_1	
	XmmcDATA0_6	GPG1[0]	56	55	GPG2[4]	XmmcDATA1_2	
	XmmcDATA0_7	GPG1[1]	58	57	GPG2[5]	XmmcDATA1_3	
	XmmcCDn0	GPG1[2]	60	59	GPG2[6]	XmmcCDn1	

CN3							
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능
CAMERA	XciDATA/MMC1DATA1	GPE0[4]	2	1	GPE0[3]	XciDATA/MMC1DATA0	CAEMRA
	XciDATA/MMC1DATA3	GPE0[6]	4	3	GPE0[5]	XciDATA/MMC1DATA2	
	XciDATA/MMC1DATA5	GPE1[0]	6	5	GPE0[7]	XciDATA/MMC1DATA4	
	XciDATA/MMC1DATA7	GPE1[2]	8	7	GPE1[1]	XciDATA/MMC1DATA6	
EBI	Xm0ADDR0	GPL0[0]	10	9	GPL2[5]	Xm0DATA0	EBI
	Xm0ADDR1	GPL0[1]	12	11	GPL2[6]	Xm0DATA1	
	Xm0ADDR2	GPL0[2]	14	13	GPL2[7]	Xm0DATA2	
	Xm0ADDR3	GPL0[3]	16	15	GPL3[0]	Xm0DATA3	
GND	GND	-	18	17	-	GND	GND
EBI	Xm0ADDR4	GPL0[4]	20	19	GPL3[1]	Xm0DATA4	EBI
	Xm0ADDR5	GPL0[5]	22	21	GPL3[2]	Xm0DATA5	
	Xm0ADDR6	GPL0[6]	24	23	GPL3[3]	Xm0DATA6	
	Xm0ADDR7	GPL0[7]	26	25	GPL3[4]	Xm0DATA7	
	Xm0ADDR8	GPL1[0]	28	27	GPL3[5]	Xm0DATA8	
	Xm0ADDR9	GPL1[1]	30	29	GPL3[6]	Xm0DATA9	
	Xm0ADDR10	GPL1[2]	32	31	GPL3[7]	Xm0DATA10	
	Xm0CSn0	GPK0[0]	34	33	GPL4[0]	Xm0DATA11	
	Xm0CSn1	GPK0[1]	36	35	GPL4[1]	Xm0DATA12	
	Xm0CSn3/FCSn1/OND	GPK0[3]	38	37	GPL4[2]	Xm0DATA13	
	Xm0CSn4/FCSn2/CFCS	GPK0[4]	40	39	GPL4[3]	Xm0DATA14	
	Xm0CSn5/FCSn3/CFCS	GPK0[5]	42	41	GPL4[4]	Xm0DATA15	
	Xm0OEn	GPK0[6]	44	43	GPK1[1]	Xm0BE1	
	Xm0WEn	GPK0[7]	46	45	GPK1[0]	Xm0BE0	
	Xm0WAITn	GPK1[2]	48	47	GPK1[3]	Xm0DATA_RDn	
CF	Xm0CF_OEn	GPK1[4]	50	49	GPK3[3]	Xm0CF_INPACKn	
GND	GND	-	52	51	-	GND	GND
CF	Xm0CF_WEn	GPK1[5]	54	53	GPK3[4]	Xm0CF_REG	CF
	Xm0CF_IORDn	GPK3[0]	56	55	GPK3[5]	Xm0CF_CDn	
	Xm0CF_INTRQ	GPK3[1]	58	57	GPK3[7]	Xm0CF_IOWRn	
	Xm0CF_RESET	GPK3[2]	60	59	GPK3[6]	Xm0CF_IORDY	

CN4							
기능	핀 이름	GPI 0	핀 번호		GPI0	핀 이름	기능
INTERRUPT	XEINT27/KP_ROW3	GP	2	1	GPH2[0]	XEINT16/KP_COL0	INTERRUPT
	XEINT19/KP_COL3	GP	4	3	GPH2[6]	XEINT22/KP_COL6	
	XEINT17/KP_COL1	GP	6	5	GPH2[4]	XEINT20/KP_COL4	
	XEINT13/CG_GPO1	GP	8	7	GPH2[2]	XEINT18/KP_COL2	
	XEINT15/CG_GPO3	GP	10	9	GPH3[0]	XEINT24/KP_ROW0	
Test Fn	CG_CLK	-	12	11	GPH3[6]	XEINT30/KP_ROW6	
INTERRUPT	XEINT11/CG_IMGIN	GP	14	13	GPH3[4]	XEINT28/KP_ROW4	
	XEINT31/KP_ROW7	GP	16	15	GPH3[2]	XEINT26/KP_ROW2	
GND	GND	-	18	17	-	GND	GND
INTERRUPT	XEINT10/CG_REALIN	GP	20	19	-	XadcAIN1	ADC
USB OTG	XuoDP	-	22	21	-	XadcAIN3	
	XuoDM	-	24	23	-	XadcAIN5	
	XuoVBUS	-	26	25	-	XadcAIN0	
	XuoDRVVBUS	-	28	27	-	XadcAIN2	
I2S/PCM	Xi2sCLK1/PCM_SCLK	GP	30	29	-	XadcAIN4	USB HOST
	Xi2sCDCLK1/PCM_EX	GP	32	31	-	XuhDP	
	Xi2sLRCK1/PCM_FSY	GP	34	33	-	XuhDN	INTERRUPT
	Xi2sSDI1/PCM_SIN1	GP	36	35	GPH1[4]	XEINT12/CG_GPO0	
	Xi2sSDO1/PCM_SOUT1/AC	GP	38	37	GPH1[6]	XEINT14/CG_GPO2	INTERRUPT
INTERRUPT	XEINT29/KP_ROW5	GP	40	39	-	XuoID	
	XEINT25/KP_ROW1	GP	42	41	GPH1[1]	XEINT9	
	XEINT23/KP_COL7	GP	44	43	-	Xi2sCLK0	
	XEINT21/KP_COL5	GP	46	45	-	Xi2sCDCLK0	
MMC	XmmcCLK2/PCM_SCLK0	GP	48	47	-	Xi2sLRCLK0	I2S
	XmmcCMD2/PCM_EXTCLK	GP	50	49	-	Xi2sSDI0	
	XmmcDATA2_0/PCM_FSYN	GP	52	51	-	Xi2sSDO0_0	
	XmmcDATA2_1/PCM_SIN0	GP	54	53	-	Xi2sSDO0_1	
	XmmcDATA2_2/PCM_SOUT	GP	56	55	-	Xi2sSDO0_2	
	XmmcDATA2_3/SPDIF_OUT	GP	58	57	-	XPWRRGTON	SYS Fn
	XmmcCDn2/SPDIF_CLK	GP	60	59	-	GND	GND

CN5							
기능	핀 이름	GP10	핀 번호		GP10	핀 이름	기능
DIPI	XmipiCLK_RXP	-	2	1	-	XmipiCLK_TXP	MIPI
	XmipiCLK_RXN	-	4	3	-	XmipiCLK_TXN	
	XmipiDP3	-	6	5	-	XmipiDP0	
	XmipiDN3	-	8	7	-	XmipiDN0	
	XmipiDP4	-	10	9	-	XmipiDP1	
	XmipiDN4	-	12	11	-	XmipiDN1	
CF	XmsmREn/CF_IORDn	GPJ4[2]	14	13	-	XmipiDP2	
	XmsmWEn/CF_CSn1	GPJ4[1]	16	15	-	XmipiDN2	
	XmsmCSn/CF_CSn0	GPJ4[0]	18	17	GPJ4[3]	XmsmIRQn/CF_IOWRn	
	XmsmADDR1	GPJ0[1]	20	19	GPJ0[0]	XmsmADDR0	
	XmsmADDR3	GPJ0[3]	22	21	GPJ0[2]	XmsmADDR2	
GND	GND	-	24	23	GPJ0[4]	XmsmADDR4	CF
CF	XmsmADDR5	GPJ0[5]	26	25	GPJ0[6]	XmsmADDR6	
	XmsmADDR7	GPJ0[7]	28	27	GPJ1[0]	XmsmADDR8	
	XmsmADDR9	GPJ1[1]	30	29	GPJ1[2]	XmsmADDR10	
	XmsmADDR11	GPJ1[3]	32	31	GPJ1[4]	XmsmADDR12	
	XmsmDATA/CF_DATA1	GPJ2[1]	34	33	GPJ2[0]	XmsmDATA/CF_DATA0	
	XmsmDATA/CF_DATA3	GPJ2[3]	36	35	GPJ2[2]	XmsmDATA/CF_DATA2	
	XmsmDATA/CF_DATA5	GPJ2[5]	38	37	GPJ2[4]	XmsmDATA/CF_DATA4	
	XmsmDATA/CF_DATA7	GPJ2[7]	40	39	GPJ2[6]	XmsmDATA/CF_DATA6	
	XmsmDATA/CF_DATA9	GPJ3[1]	42	41	GPJ3[0]	XmsmDATA/CF_DATA8	
GND	GND	-	44	43	-	GND	GND
CF	XmsmDATA/CF_DATA11	GPJ3[3]	46	45	GPJ3[2]	XmsmDATA/CF_DATA10	CF
	XmsmDATA/CF_DATA13	GPJ3[5]	48	47	GPJ3[4]	XmsmDATA/CF_DATA12	
	XmsmDATA/CF_DATA15	GPJ3[7]	50	49	GPJ3[6]	XmsmDATA/CF_DATA14	
EBI	Xm0ADDR12	GPL1[4]	52	51	GPL1[3]	Xm0ADDR11	EBI
	Xm0ADDR14	GPL1[6]	54	53	GPL1[5]	Xm0ADDR13	
	Xm0ADDR16	GPL2[0]	56	55	GPL1[7]	Xm0ADDR15	
	Xm0ADDR18	GPL2[2]	58	57	GPL2[1]	Xm0ADDR17	
	Xm0ADDR20	GPL2[4]	60	59	GPL2[3]	Xm0ADDR19	