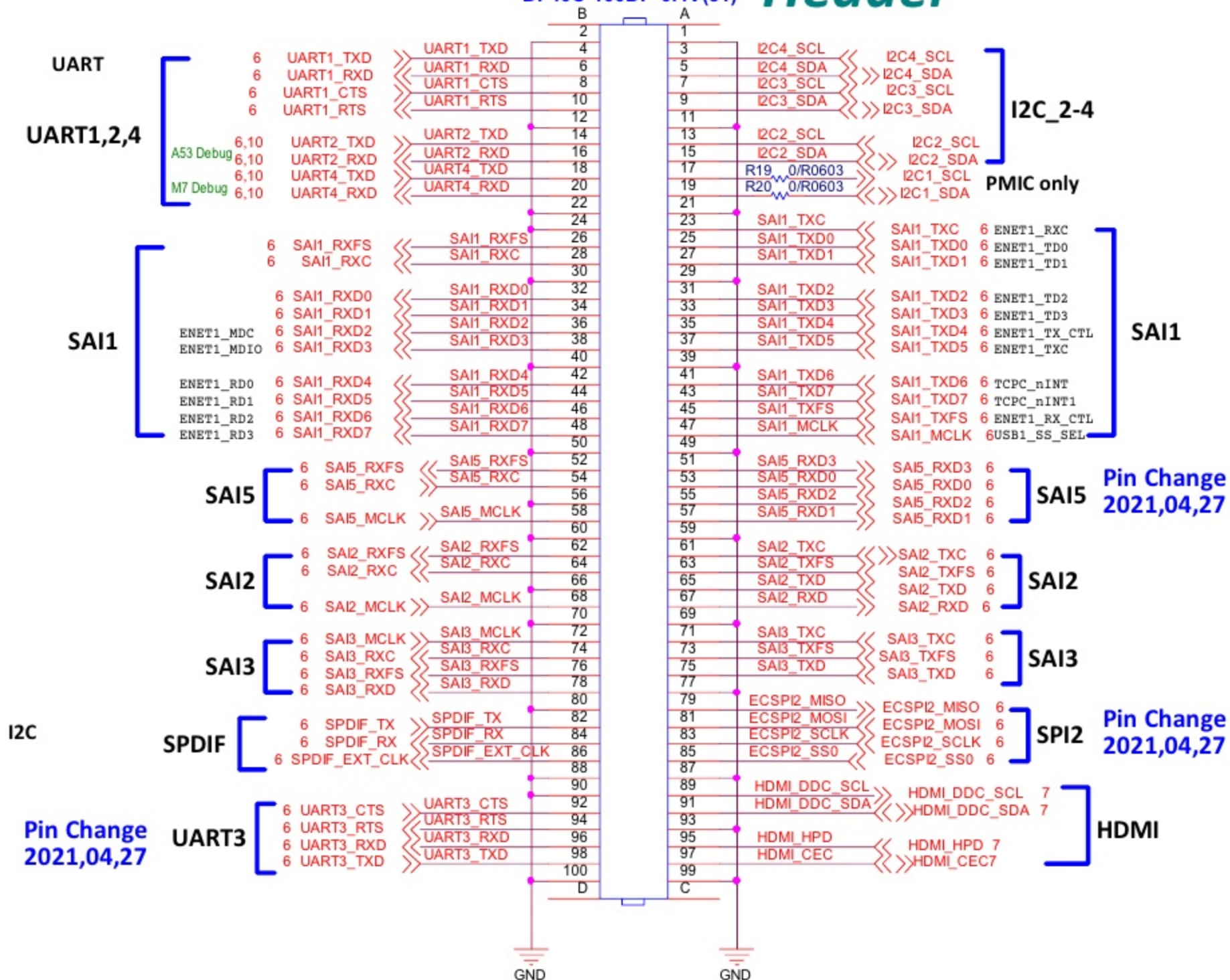


DF40C(2.0)-80DS-0.4V(S)/0.4mm-Hirose

CN1

DF40C-100DP-0.4V(51)

Header

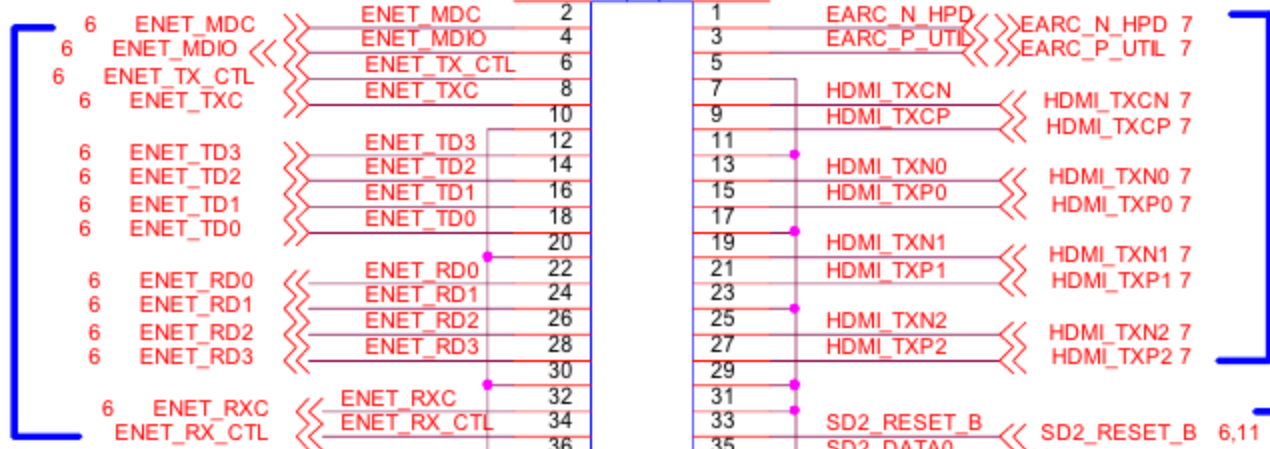


Header

CN2

DF40C-100DP-0.4V(51)

Ethernet
Pin Change
2021,04,27



HDMI

SDIO2

SDIO2
Pin Change
2021,04,27

VSYS_5V

VSYS_5V

VDD_3V3

VDD_1V8

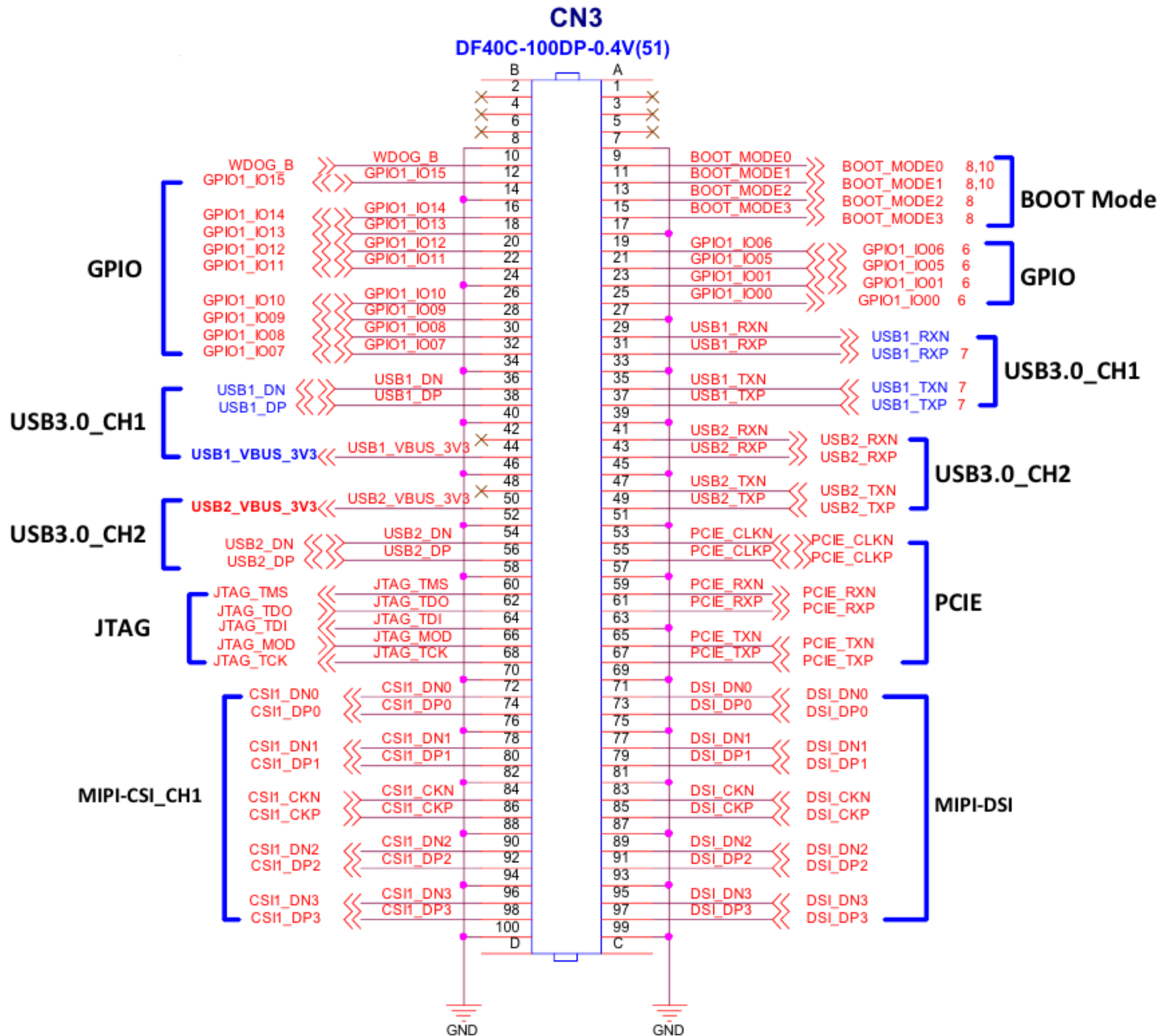
VSD_3V3

PMIC

GND

GND

Header



CN4

DF40C-100DP-0.4V(51)

Header

MIPI-CSI_CH2

7 CSI2_DN3
7 CSI2_DP3
7 CSI2_DN2
7 CSI2_DP2
7 CSI2_CKN
7 CSI2_CKP
7 CSI2_DN1
7 CSI2_DP1
7 CSI2_DN0
7 CSI2_DP0

CSI2_DN3
CSI2_DP3
CSI2_DN2
CSI2_DP2
CSI2_CKN
CSI2_CKP
CSI2_DN1
CSI2_DP1
CSI2_DN0
CSI2_DP0

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NAND_DQS
ONOFF
POR_B
PMIC_ON_REQ

NAND_DQS
ONOFF
POR_B
PMIC_ON_REQ

CLKIN1
CLKOUT1
CLKIN2
CLKOUT2

CLK

QSPI

6 QSPIA_DATA2
6 QSPIA_DATA1
6 QSPIA_DATA0
6 QSPIA_DATA3

QSPIA_DATA2
QSPIA_DATA1
QSPIA_DATA0
QSPIA_DATA3

92
94
96
98
100

D

GND

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C

LVDS1_TX0_P
LVDS1_TX0_N
LVDS1_TX1_P
LVDS1_TX1_N
LVDS1_CLK_P
LVDS1_CLK_N
LVDS1_TX2_P
LVDS1_TX2_N
LVDS1_TX3_P
LVDS1_TX3_N

LVDS1_TX0_P
LVDS1_TX0_N
LVDS1_TX1_P
LVDS1_TX1_N
LVDS1_CLK_P
LVDS1_CLK_N
LVDS1_TX2_P
LVDS1_TX2_N
LVDS1_TX3_P
LVDS1_TX3_N

LVDS_CH1

LVDS0_TX0_P
LVDS0_TX0_N
LVDS0_TX1_P
LVDS0_TX1_N
LVDS0_CLK_P
LVDS0_CLK_N
LVDS0_TX2_P
LVDS0_TX2_N
LVDS0_TX3_P
LVDS0_TX3_N

SD1_STROBE
SD1_RESET_B
SD1_CLK
SD1_CMD
SD1_DATA0
SD1_DATA1
SD1_DATA2
SD1_DATA3
SD1_DATA4
SD1_DATA5
SD1_DATA6
SD1_DATA7

SD1_STROBE
SD1_RESET_B
SD1_CLK
SD1_CMD
SD1_DATA0
SD1_DATA1
SD1_DATA2
SD1_DATA3
SD1_DATA4
SD1_DATA5
SD1_DATA6
SD1_DATA7

LVDS_CH0

SD_CH1

QSPI

QSPIA_nSS0 6
QSPIA_SCLK 6

GND