

Board Connector Pin Map

CN1(1~50)								
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능	
Power	Main Batt	Main Batt	1	2	Main Batt	Main Batt	Power	
Power	Main Batt	Main Batt	3	4	Main Batt	Main Batt	Power	
Power	VDD_33 OFF	VDD_33 OFF	5	6	VDD_33 OFF	VDD_33 OFF	Power	
Power	VDD_33 ON	VDD_33 ON	7	8	VDD_33 ON	VDD_33 ON	Power	
GND	GND	GND	9	10	VDDQ_CAM	VDDQ_CAM	Power	
JTAG	XjTRSTn	JTAG	11	12	VDD_RTC_AP	VDD_RTC_AP	Power	
	XjTMS	JTAG	13	14	USB_Host	XuhPWREN	USB	
	XjTCK	JTAG	15	16	USB_Host	XuhOVERCUR		
	XjTDI	JTAG	17	18	USB_Host	XuoDRVVBUS		
	XjTDO	JTAG	19	20	MIPI-Slave	XmipiSDP3	MiPi slave	
Camera	XciPCLK	GPJ0[0]	21	22	MIPI-Slave	XmipiSDN3		
	GND	GND	23	24	MIPI-Slave	XmipiSDP2		
	XciVSYNC	GPJ0[1]	25	26	MIPI-Slave	XmipiSDN2		
	XciHREF	GPJ0[2]	27	28	MIPI-Slave	XmipiSDP1		
	XciYDATA0	GPJ0[3]	29	30	MIPI-Slave	XmipiSDN1		
	XciYDATA1	GPJ0[4]	31	32	MIPI-Slave	XmipiSDP0		
	XciYDATA2	GPJ0[5]	33	34	MIPI-Slave	XmipiSDN0		
	XciYDATA3	GPJ0[6]	35	36	GND	GND		
	XciYDATA4	GPJ0[7]	37	38	MIPI-Slave	XmipiSDPCLK		
	XciYDATA5	GPJ1[0]	39	40	MIPI-Slave	XmipiSDNCLK		
	XciYDATA6	GPJ1[1]	41	42	GND	GND		GND
	XciYDATA7	GPJ1[2]	43	44	MIPI-Master	XmipiMDP3		MiPi master
	XciCLKend	GPJ1[3]	45	46	MIPI-Master	XmipiMDN3		
XciFIELD	GPJ1[4]	47	48	MIPI-Master	XmipiMDP2			
GND	GND	GND	49	50	MIPI-Master	XmipiMDN2		

CN1(51~100)								
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능	
PWM	XpwmTOUT3/i2cSCL7	GPD0[3]	51	52	MIPI-Master	XmipiMDP1	MiPi master	
	XpwmTOUT2/i2cSDA7	GPD0[2]	53	54	MIPI-Master	XmipiMDN1		
	XpwmTOUT1/LCD_PWM	GPD0[1]	55	56	MIPI-Master	XmipiMDP0		
	XpwmTOUT0/LCD_FRM	GPD0[0]	57	58	MIPI-Master	XmipiMDN0		
I2C	Xi2cSCL1/MIPI2_ESC_CLK	GPD1[3]	59	60	GND	GND		
	Xi2cSDA1/MIPI2_BYTE_CLK	GPD1[2]	61	62	MIPI-Master	XmipiMDPCLK		
	Xi2cSCL0/MIPI2_ESC_CLK	GPD1[1]	63	64	MIPI-Master	XmipiMDNCLK		
	Xi2cSDA0/MIPI2_BYTE_CLK	GPD1[0]	65	66	GND	GND		
GND	GND	GND	67	68	MIPI-Master	Xmipi2LMDP1		GND
UART	XuRXD0	GPA0[0]	69	70	MIPI-Master	Xmipi2LMDN1		
	XuTXD0	GPA0[1]	71	72	MIPI-Master	Xmipi2LMDP0		
	XuCTSn0	GPA0[2]	73	74	MIPI-Master	Xmipi2LMDN0		
	XuRTSn0	GPA0[3]	75	76	MIPI-Master	Xmipi2LMDPCLK		
	XuRXD3	GPA1[4]	77	78	MIPI-Master	Xmipi2LMDNCLK		
XuTXD3	GPA1[5]	79	80	GND	GND			
GND	GND	GND	81	82	MIPI-Slave	Xmipi2LSDP1	MiPi slave	
HDMI	XhdmiTX0P	HDMI	83	84	MIPI-Slave	Xmipi2LSDN1		
	XhdmiTX0N	HDMI	85	86	MIPI-Slave	Xmipi2LSDP0		
	XhdmiTX1P	HDMI	87	88	MIPI-Slave	Xmipi2LSDN0		
	XhdmiTX1N	HDMI	89	90	GND	GND		
	XhdmiTX2P	HDMI	91	92	MIPI-Slave	Xmipi2LSDPCLK		
	XhdmiTX2N	HDMI	93	94	MIPI-Slave	Xmipi2LSDNCLK		
	GND	GND	GND	95	96	GND	GND	
HDMI	XhdmiTXCP	HDMI	97	98	SLIM_DAT	XsbusData/SLIM_D	SLIM Bus	
	XhdmiTXCN	HDMI	99	100	SLIM_CLK	XsbusClk/SLIM_CLK		

CN2(1~50)							
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능
I2S & AC97	Xi2Ssclk1/AC97_BITCLK	GPC0[0]	1	2	GPC1[0]	Xi2sSCLK2/PCM_SCLK2	I2S & PCM
	Xi2sCDCLK1/AC_RESET	GPC0[1]	3	4	GPC1[1]	Xi2sCDCLK2/PCM_EXTCL	
	Xi2sLRCK1/AC97_SYNC	GPC0[2]	5	6	GPC1[2]	Xi2sLRCK2/PCM_FSYNC2	
	Xi2sSDI1/AC97_SDI	GPC0[3]	7	8	GPC1[3]	Xi2sSDI2/PCM_SIN2	
	Xi2sSDO1/AC97_SDO	GPC0[4]	9	10	GPC1[4]	Xi2sSDO2/PCM_SOUT2	
MMC	Xmmc3CLK	GPK3[0]	11	12	VDD	VDD_MMC	MMC
	Xmmc3CMD	GPK3[1]	13	14	GPK2[0]	Xmmc2CLK	
	Xmmc3CDn	GPK3[2]	15	16	GPK2[1]	Xmmc2CMD	
	Xmmc3DATA0/SD2_DA	GPK3[3]	17	18	GPK2[2]	Xmmc2CDn	
	Xmmc3DATA1/SD2_DA	GPK3[4]	19	20	GPK2[3]	Xmmc2DATA_0	
	Xmmc3DATA2/SD2_DA	GPK3[5]	21	22	GPK2[4]	Xmmc2DATA_1	
	Xmmc3DATA3/SD2_DA	GPK3[6]	23	24	GPK2[5]	Xmmc2DATA_2	
	Xmmc1CLK	GPK1[0]	25	26	GPK2[6]	Xmmc2DATA_3	
	Xmmc1CMD	GPK1[1]	27	28	GPK1[2]	Xmmc1CDn	
Memory 0	Xm0FRnB_1	GPY2[3]	29	30	GPY2[0]	Xm0FCLE	Memory 0
	Xm0FRnB_2	GPY2[4]	31	32	GPY2[1]	Xm0FALE	
	Xm0FRnB_3	GPY2[5]	33	34	GPY2[2]	Xm0FRnB_0	
	GND	GND	35	36	GND	GND	
	Xm0DATA0	GPY5[0]	37	38	GPY3[0]	Xm0ADDR0	
	Xm0DATA1	GPY5[1]	39	40	GPY3[1]	Xm0ADDR1	
	Xm0DATA2	GPY5[2]	41	42	GPY3[2]	Xm0ADDR2	
	Xm0DATA3	GPY5[3]	43	44	GPY3[3]	Xm0ADDR3	
	Xm0DATA4	GPY5[4]	45	46	GPY3[4]	Xm0ADDR4	
	Xm0DATA5	GPY5[5]	47	48	GPY3[5]	Xm0ADDR5	
	Xm0DATA6	GPY5[6]	49	50	GPY3[6]	Xm0ADDR6	

CN2(51~100)								
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능	
Memory 0	Xm0DATA7	GPY5[7]	51	52	GPY3[7]	Xm0ADDR7	Memory 0	
	Xm0DATA8	GPY6[0]	53	54	GND	GND		
	Xm0DATA9	GPY6[1]	55	56	GPY4[0]	Xm0ADDR8		
	Xm0DATA10	GPY6[2]	57	58	GPY4[1]	Xm0ADDR9		
	Xm0DATA11	GPY6[3]	59	60	GPY4[2]	Xm0ADDR10		
	Xm0DATA12	GPY6[4]	61	62	GPY4[3]	Xm0ADDR11		
	Xm0DATA13	GPY6[5]	63	64	GPY4[4]	Xm0ADDR12		
	Xm0DATA14	GPY6[6]	65	66	GPY4[5]	Xm0ADDR13		
	Xm0DATA15	GPY6[7]	67	68	GPY4[6]	Xm0ADDR14		
	Xm0CSn_0	GPY0[0]	69	70	GPY4[7]	Xm0ADDR15		
	Xm0CSn_1	GPY0[1]	71	72	GPY0[2]	Xm0CSn_2		
	Xm0WAITn	GPY1[2]	73	74	GPY0[3]	Xm0CSn_3		
	Xm0DATA_RDn	GPY1[3]	75	76	GPY0[4]	Xm0OEn		
GND	GND	GND	77	78	GPY0[5]	Xm0WEn		
PCie	XpcieREFCLKP	PCie	79	80	GPY1[1]	Xm0BEn_1	SATA	
	XpcieREFCLKM	PCie	81	82	GPY1[0]	Xm0BEn_0		
	GND	GND	83	84	GND	GND		GND
	XpcieTX2P	PCie	85	86	SATA	XsataREFCLKP		
	XpcieTX2M	PCie	87	88	SATA	XsataREFCLKM		
	GND	GND	89	90	GND	GND		
	XpcieRX2P	PCie	91	92	SATA	XsataTX2P		
	XpcieRX2M	PCie	93	94	SATA	XsataTX2M		
	GND	GND	95	96	GND	GND		
	XpcieCK100P	PCie	97	98	SATA	XsataRX2P		
XpcieCK100N	PCie	99	100	SATA	XsataRX2M			

CN3(1~50)														
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능							
LCD	XvVD_0	GPF0[4]	1	2	GPF2[0]	XvVD_12	LCD							
	XvVD_1	GPF0[5]	3	4	GPF2[1]	XvVD_13								
	XvVD_2	GPF0[6]	5	6	GPF2[2]	XvVD_14								
	XvVD_3	GPF0[7]	7	8	GPF2[3]	XvVD_15								
	XvVD_4	GPF1[0]	9	10	GPF2[4]	XvVD_16								
	XvVD_5	GPF1[1]	11	12	GPF2[5]	XvVD_17								
	XvVD_6	GPF1[2]	13	14	GPF2[6]	XvVD_18								
	XvVD_7	GPF1[3]	15	16	GPF2[7]	XvVD_19								
	XvVD_8	GPF1[4]	17	18	GPF3[0]	XvVD_20								
	XvVD_9	GPF1[5]	19	20	GPF3[1]	XvVD_21								
	XvVD_10	GPF1[6]	21	22	GPF3[2]	XvVD_22								
	XvVD_11	GPF1[7]	23	24	GPF3[3]	XvVD_23								
	XvVSYNC_LDI	GPF3[4]	25	26	GND	GND		GND						
	XvSYS_OE	GPF3[5]	27	28	DAC	DACOUT		DACOUT						
GND	GND	GND	37	38	GPA0[7]	XuRTSn1/i2cSCL2	UART & I2C							
								XnRSTOUT	XnRSTOUT	UART & I2C				
								XvVSYNC	GPF0[1]		31	32	GPA0[4]	XuRXD1
								XvVDEN	GPF0[2]		33	34	GPA0[5]	XnTXD1
								XvVCLK	GPF0[3]		35	36	GPA0[6]	XuCTSn1/i2cSDA2
								XmsmWEn	GPE0[0]		39	40	GPA1[0]	XuRXD2
Modem	XmsmCSn	GPE0[1]	41	42	GPA1[1]	XuTXD2	Modem							
	XmsmRn	GPE0[2]	43	44	GPA1[2]	XuCTSn2/i2cSDA3								
	XmsmIRQn	GPE0[3]	45	46	GPA1[3]	XuRTSn2/i2cSCL3								
	XmsmADVN	GPE0[4]	47	48	GND	GND		GND						
	XmsmADDR[0]	GPE1[0]	49	50	GPE3[0]	XmsmDATA[0]		Modem						

CN3(51~100)								
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능	
Modem	XmsmADDR[1]	GPE1[1]	51	52	GPE3[1]	XmsmDATA[1]	Modem	
	XmsmADDR[2]	GPE1[2]	53	54	GPE3[2]	XmsmDATA[2]		
	XmsmADDR[3]	GPE1[3]	55	56	GPE3[3]	XmsmDATA[3]		
	GND	GND	57	58	GPE3[4]	XmsmDATA[4]		
	XmsmADDR[4]	GPE1[4]	59	60	GPE3[5]	XmsmDATA[5]		
	XmsmADDR[5]	GPE1[5]	61	62	GPE3[6]	XmsmDATA[6]		
	XmsmADDR[6]	GPE1[6]	63	64	GPE3[7]	XmsmDATA[7]		
	XmsmADDR[7]	GPE1[7]	65	66	GPE4[0]	XmsmDATA[8]		
	XmsmADDR[8]	GPE2[0]	67	68	GND	GND		
	XmsmADDR[9]	GPE2[1]	69	70	GPE4[1]	XmsmDATA[9]		
	XmsmADDR[10]	GPE2[2]	71	72	GPE4[2]	XmsmDATA[10]		
	XmsmADDR[11]	GPE2[3]	73	74	GPE4[3]	XmsmDATA[11]		
	XmsmADDR[12]	GPE2[4]	75	76	GPE4[4]	XmsmDATA[12]		
	XmsmADDR[13]	GPE2[5]	77	78	GPE4[5]	XmsmDATA[13]		
GND	GND	GND	79	80	GPE4[6]	XmsmDATA[14]		
Touch & ADC	XadcAIN9	ADC	81	82	GPE4[7]	XmsmDATA[15]	SPI	
	XadcAIN8	ADC	83	84	GND	GND		GND
	XadcAIN7	ADC	85	86	GPB[4]	XspiCLK[1]		
	XadcAIN6	ADC	87	88	GPB[5]	XspiCSn[1]		
	XadcAIN5	ADC	89	90	GPB[6]	XspiMISO[1]		
	XadcAIN4	ADC	91	92	GPB[7]	XspiMOSI[1]		
	XadcAIN3	ADC	93	94	GPB[0]	XspiCLK[0]		
	XadcAIN2	ADC	95	96	GPB[1]	XspiCSn[0]		
	XadcAIN1	ADC	97	98	GPB[2]	XspiMISO[0]		
	XadcAIN0	ADC	99	100	GPB[3]	XspiMOSI[0]		

CN5(1~80)							
기능	핀 이름	GPIO	핀 번호		GPIO	핀 이름	기능
GPS	XGNSS_GPIO_7	GPL2[7]	1	2	USB_host	XuhDP	USB
	XGNSS_SYNC	GPL0[0]	3	4	USB_host	XuhDN	
	XGNSS_ISIGN	GPL0[1]	5	6	GND	GND	
	XGNSS_IMAG	GPL0[2]	7	8	USB_OTG	XuoDP	
	XGNSS_QSIGN	GPL0[3]	9	10	USB_OTG	XuoDN	
	XGNSS_QMAG	GPL0[4]	11	12	GND	GND	
	XGNSS_MCLK	GPL0[5]	13	14	USB_OTG	XuoID	
	XGNSS_RF_RSTN	GPL0[6]	15	16	USB_OTG	XuoVBUS	
	XGNSS_CLKREQ	GPL0[7]	17	18	HSIC	XuhostSTROBE1	HSIC
	XGNSS_SCL	GPL1[0]	19	20	HSIC	XuhostDATA1	
	XGNSS_SDA	GPL1[1]	21	22	HSIC	XuhostSTROBE2	
	XGNSS_EPOCH	GPL1[2]	23	24	HSIC	XuhostDATA2	
	XGNSS_RTC_OUT	-	25	26	XnRESET	XnRESET	XnRESET
	I2S	Xi2s0SCLK	GPZ[0]	27	28	XPWRRGTON	XPWRRGTON
Xi2s0CDCLK		GPZ[1]	29	30	XPSHOLD	XPSHOLD	XPSHOLD
Xi2s0LRCK		GPZ[2]	31	32	GPL2[0]	XGNSS_GPIO_0	GPS
Xi2s0SDI		GPZ[3]	33	34	GPL2[1]	XGNSS_GPIO_1	
Xi2s0SDO_0		GPZ[4]	35	36	GPL2[2]	XGNSS_GPIO_2	
Xi2s0SDO_1		GPZ[5]	37	38	GPL2[3]	XGNSS_GPIO_3	
Xi2s0SDO_2		GPZ[6]	39	40	GPL2[4]	XGNSS_GPIO_4	
GND	GND	GND	41	42	GPL2[5]	XGNSS_GPIO_5	
Boot Select	XOM1	XOM1	43	44	GPL2[6]	XGNSS_GPIO_6	
	XOM2	XOM2	45	46	GPX0[0]	XEINT_0	Interrupt
	XOM3	XOM3	47	48	GPX0[1]	XEINT_1	
	XOM4	XOM4	49	50	GPX0[2]	XEINT_2	
	XOM5	XOM5	51	52	GPX0[3]	XEINT_3	
Interrupt	XEINT_18	GPX2[2]	53	54	GPX0[4]	XEINT_4	
	XEINT_19	GPX2[3]	55	56	GPX0[5]	XEINT_5	
	XEINT_20	GPX2[4]	57	58	GPX0[6]	XEINT_6	
	XEINT_21	GPX2[5]	59	60	GPX0[7]	XEINT_7	
	XEINT_22	GPX2[6]	61	62	GPX1[0]	XEINT_8	
	XEINT_23	GPX2[7]	63	64	GPX1[1]	XEINT_9	
	XEINT_24	GPX3[0]	65	66	GPX1[2]	XEINT_10	
	XEINT_25	GPX3[1]	67	68	GPX1[3]	XEINT_11	
	XEINT_26	GPX3[2]	69	70	GPX1[4]	XEINT_12	
	XEINT_27	GPX3[3]	71	72	GPX1[5]	XEINT_13	
	XEINT_28	GPX3[4]	73	74	GPX1[6]	XEINT_14	
	XEINT_29	GPX3[5]	75	76	GPX1[7]	XEINT_15	
	XEINT_30	GPX3[6]	77	78	GPX2[0]	XEINT_16	
XEINT_31	GPX3[7]	79	80	GPX2[1]	XEINT_17		